Sr.No.	Name of the Llaboratory	No of Student Per Setup Batch	Name of the important equipment	Quantity	Lab utilization hours per week
1	Analog Electronics Lab	3 to 5 students per set in a batch of 20	Digital Storage Oscilloscope	02	10 hrs as per TE scheme
			20 MHZ Function Generator	2	
			OP-Amp trainer kit	3	
			D.C. Regulated Power Supply	14	
2	Computer Lab	1student per set in a batch of 20	Dell AIO 5400 Desktop	5	16 hrs as per TE scheme
			Lenovo think centre Pcs	15	
			HP Laser jet 1136 MFP Printer	1	
			Microsoft Windows -11 pro	05	
3	Digital & Micro Processor Lab	3 to 5 students per set in a batch of 20	8051 Microcontroller kit	5	8 hrs as per TE scheme
			Digital Logic Trainer kit	5	
			8085 microprocessor kit	06	
4	Measurement and Control Lab	per set in	DC Position Control System	1	20 hrs as per TE scheme
			AC Position Control System	1	
			PID Controller	1	
			PLC Trainer	1	
5	Communicati on Lab	3 to 5 students per set in a batch of 20	Digital Spectrum Analyzer	01	12 hrs as TE Scheme
			Micro Wave Test Bench	01	
			Mobile Trainer kit	01	
			Fibre Optic Trainer kit	01	

			Colour TV Demo kit	01	
6	Electronics Workshop Lab	3 to 5 students per set in a batch of 20	PCB Drilling Machine	1	14 hrs as per TE scheme
			Double sided UV Exposure	1	
			PCB Etching Machine	1	
			Photo Resisit Dip Coating	1	
			Roller Tinning Machine	1	
	Physics Lab	3 to 5 students per set in a batch of 20	1) P-N Junction diode setup	4	30 practical hours per week as per T-E Scheme
			2) Searle's Apparatus	4	
			3) Vernier caliper	10	
			4) Micrometer screw gauge	9	
			5) Spectro meter	2	
			6) Instrument to verify Ohm's law	2	
7			7) Travelling microscope	4	
			8) Boyle's law apparatus	3	
			9) Busense's photometer	2	
			10) Stoke's apparatus	4	
			11) Platinum resistance thermometer	2	
8	ChemistryLab	3 to 5 students per set in a batch of 20	1) Muffle Furnace	1	30 practical hours per week as
			2) Hot air Oven	1	
			3) Magnetic Stirrer	4	

4 hours
hours
per week as per T-E Scheme
) 4
hours per week as per T-E Scheme
1
04
hours per week as
per T-E
Scheme
4